

# Demonstration of real-time image processing on the SCAMP-3 vision system

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**Abstract** - Vision system based on a cellular processor array integrated circuit is presented. The system includes the SCAMP-3 vision chip, control and interface circuits, and software development environment enabling code simulation, compilation and debugging. Execution of a number of low-level image processing algorithms in real-time is demonstrated.

## INTRODUCTION

The Cellular Neural Network (CNN) model is believed to provide a useful paradigm for designing VLSI image processing systems, specifically pixel-parallel vision processors. Our work demonstrates that a device based on a different concept, that of a massively parallel array of simple sequential analogue processors, operating in a Single Instruction Multiple Data (SIMD) mode, can achieve superior performance as compared with CNN-UM chips when applied to common image processing problems [1,2]. We have recently developed the SCAMP-3 vision chip (Fig.1), which implements a 128x128 cellular processor array [3]. We have also developed a system, based on the vision chip (Fig.2a), which includes controller and interface electronics [4]. The software controlling the vision system supports algorithm development, simulation, compilation and host communication. The general-purpose nature of the system allows the implementation of many low-level image processing algorithms, examples of which are shown in Fig.2b. Filtering and feature detectors based on 3x3 convolution kernels are easily implemented. Other popular CNN-type operators, based on propagating templates (such as hole-filling), are implemented iteratively. Non-linear filtering (e.g. true median filter) as well as more complex algorithms (e.g. skeletonization and active contours, shown in Fig.3) can be implemented and executed at high frame rates and with low power consumption. These features will be demonstrated during the live session.

## REFERENCES

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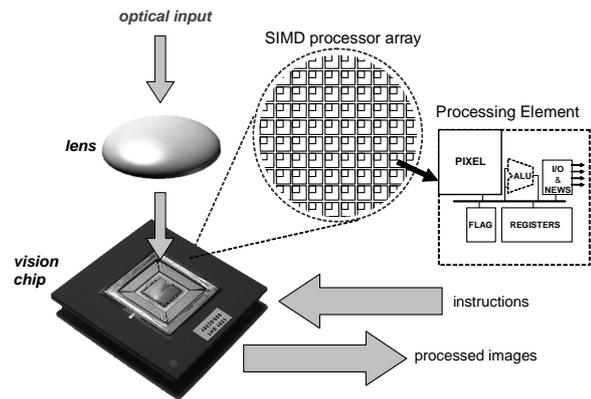


Fig.1. Overview of the SIMD pixel-parallel cellular vision chip

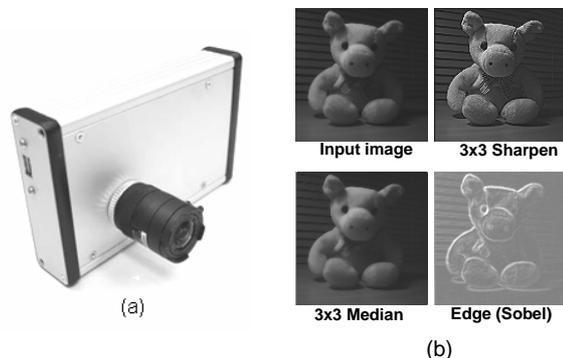


Fig.2. (a) SCAMP vision system (b) Processing results

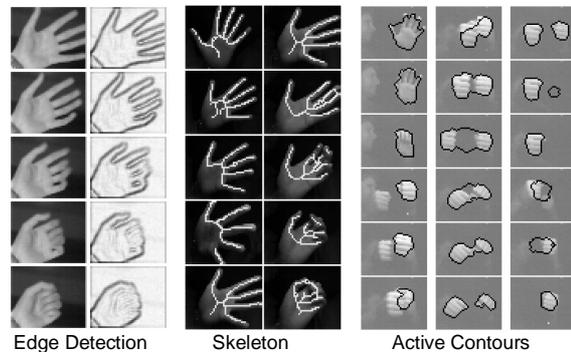


Fig.3. Processing of real-time image sequences (experimental results from 39x48 array chip are shown)